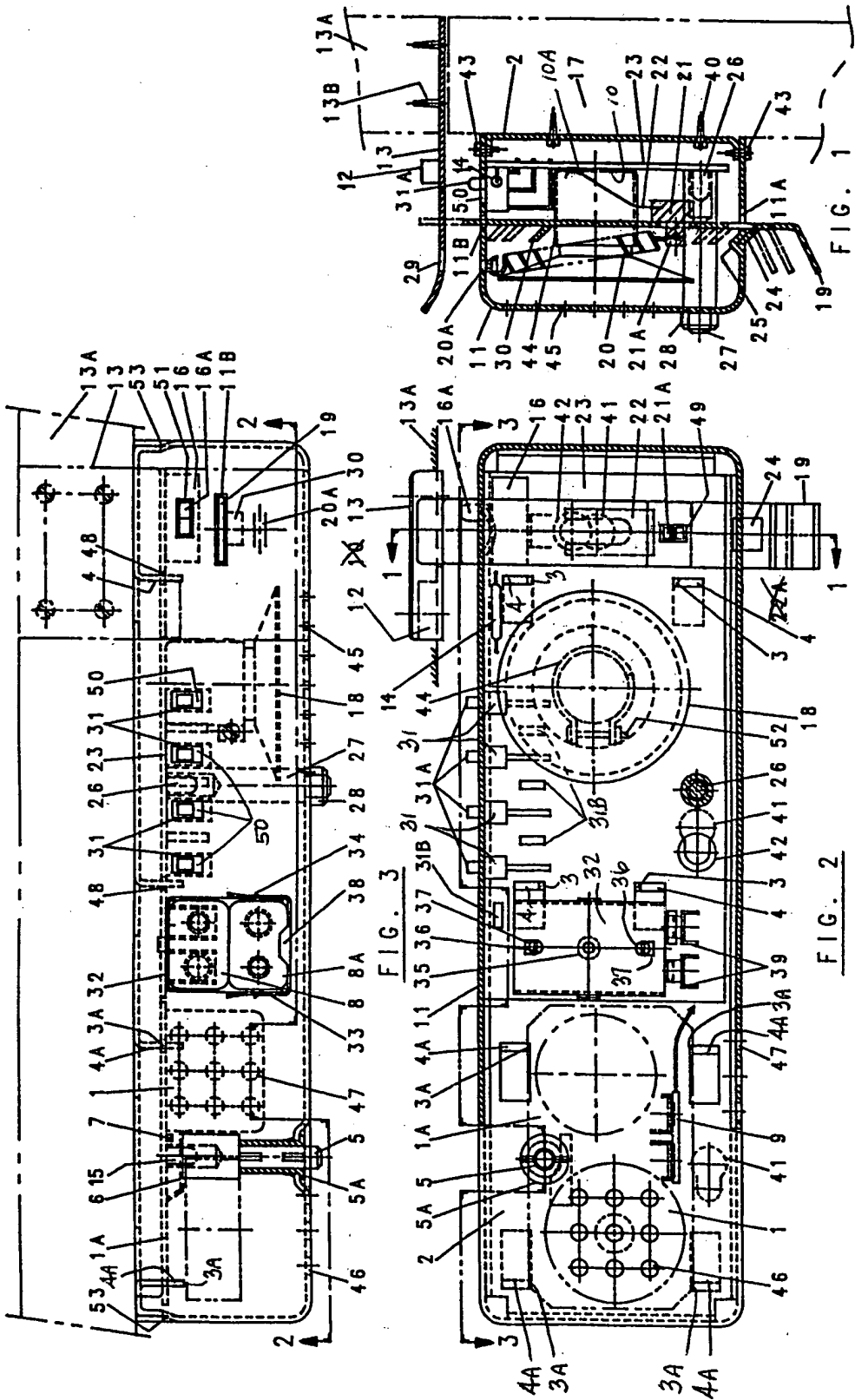




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PATENT APPLICATION  
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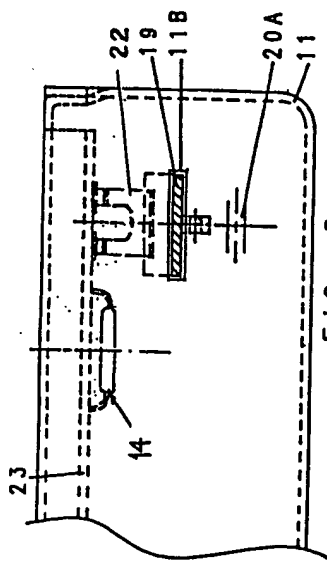


FIG. 5

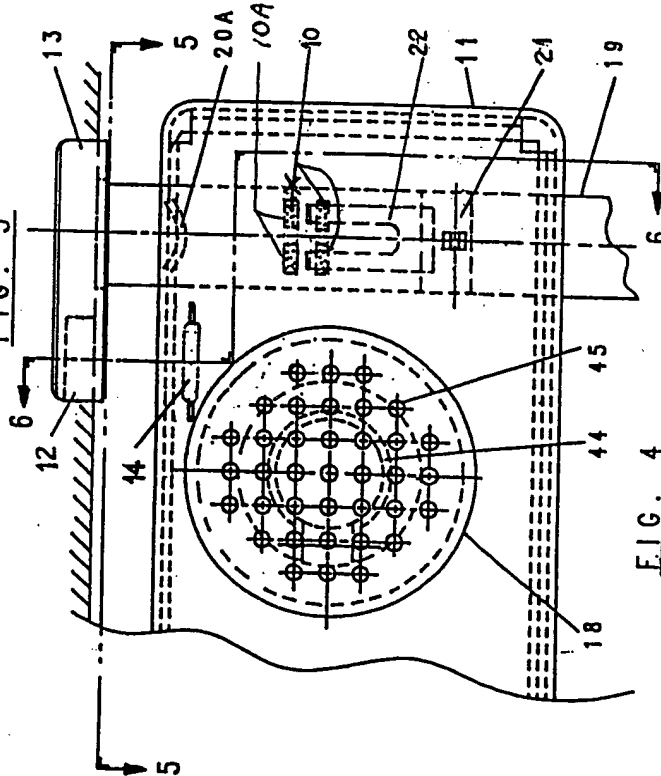


FIG. 4

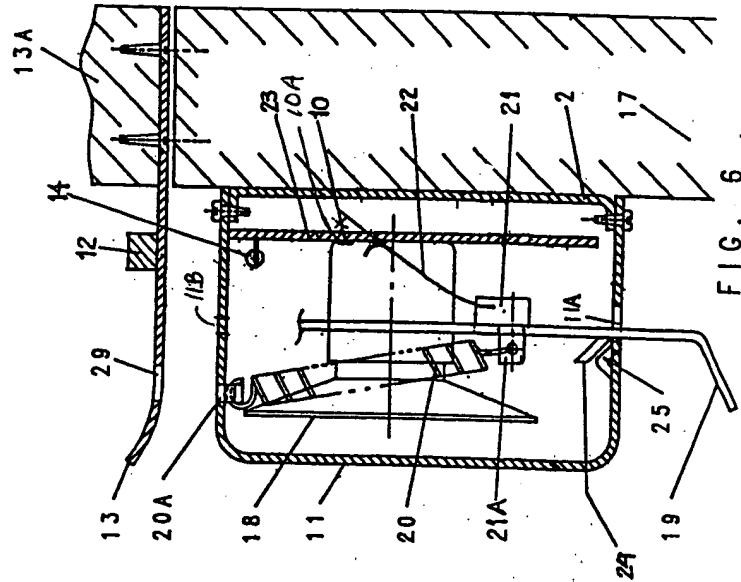


FIG. 6

The diagram illustrates a burglar alarm system. It includes a table of inputs and outputs, a main circuit diagram, and a detailed view of the alarm output stage.

up OUTPUTS	up INPUTS
ALARM SIGNAL	SW1 MODE SWITCH CHIME/ALARM
CHIM CHIME SIGNAL	S1 PREALARM SWITCH ON PCB
AUPOIN TURN ON U3	S3 BOLT SWITCH HOME ARMED ON PCB
ALARM LED BLINK	S2 DOOR SENSOR SWITCH (REED)
LOBLED FOR LOW BATTERY BLINK LED	JUMPT ALARM DELAY
	LOBATT LOW BATTERY SIGNAL

**Main Circuit Diagram:**

- Power Source:** BATTERY (10A, 22).
- Inputs:** SW1 (Mode Switch), S1 (Prealarm Switch), S2 (Door Sensor Switch), S3 (Bolt Switch), JUMPT (Alarm Delay).
- Processing:** U1 (HT28125C) and U2 (HT28205) microcontrollers. U1 handles the alarm logic, and U2 handles the chime logic.
- Outputs:** LOBLED (Low Battery LED), ALARM LED, CHIM (Chime), and AUPOIN (Alarm Point).
- Components:** Resistors (R1-R14), capacitors (C1-C5), and a speaker (SPEAKER 8 OHMS).

**Alarm Output Stage (Fig. 7):**

- Power Source:** BATTERY (10A, 22).
- Inputs:** SW1 (Mode Switch), S1 (Prealarm Switch), S2 (Door Sensor Switch), S3 (Bolt Switch), JUMPT (Alarm Delay).
- Processing:** U1 (HT28125C) and U2 (HT28205) microcontrollers. U1 handles the alarm logic, and U2 handles the chime logic.
- Outputs:** LOBLED (Low Battery LED), ALARM LED, CHIM (Chime), and AUPOIN (Alarm Point).
- Components:** Resistors (R1-R14), capacitors (C1-C5), and a speaker (SPEAKER 8 OHMS).

FIG. 7